



Docket No.: WMP-IFT631

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By: [Signature] Date: January 15, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Rainald Sander
Applic. No. : 09/943,589
Filed : August 30, 2001
Title : Circuit Arrangement to Determine the Current in a Load Transistor

CLAIM FOR PRIORITY

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

Claim is hereby made for a right of priority under Title 35, U.S. Code, Section 119, based upon the German Patent Application 100 42 585.2, filed August 30, 2000.

A certified copy of the above-mentioned foreign patent application is being submitted herewith.

Respectfully submitted,

[Signature]
For Applicant

Mark P. Weichselbaum
Reg. No. 43,248

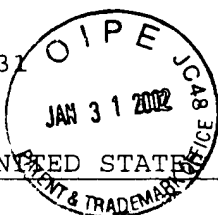
Date: January 15, 2002

Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

/bmb

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TECHNICAL CLAIMS

WMP-IFT631



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REQUEST TO APPROVE DRAWING CHANGES

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Please approve the accompanying formal drawing Figs. 2 and 3
in which labels have been added to the blocks shown therein.

Respectfully submitted,

Mark P. Weichselbaum
Reg. No. 43,248

For Applicant

MPW:cgm

Date: January 15, 2002

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